

from the Schmitt inverter gate 14 goes low, the output from the NAND Schmitt gate 11 goes low. Therefore, oscillation of the first pulse generator C is stopped. In this case, since one input signal of the OR gate 12 goes low, the output signal from the OR gate 12 goes low regardless of the input signal from the second pulse generator D. Thus, the signal processor B is kept cleared. In this case, since no pulse is input from the first pulse generator C, no carry signal is supplied from the signal processor B to the second pulse generator D.

Please replace paragraph [0062] with the following paragraph:

--[0062] Fig. 10B is a schematic sectional view showing an accessory 100A according to the second embodiment of the present invention. In the figure, the transparent body holding member 300 comprises a heat collecting member body 301 composed of a good conductive metal such as aluminum or silver and uniform fixing portion 302 made of shape memory alloy. The fixing portion 302 is attached to the tip portion of the heat collecting member body 301 by means of caulking or brazing.--.

Please replace paragraph [0067] with the following paragraph:

--[0067] Fig. 11A depicts the states where the transparent body 1 is detachably attached by the fixing portion 306 of the transparent body holding member 305. In the figure, the transparent body 1 of the accessory 100A' is detachably